

PCN Number:	20131007000	PCN Date:	10/10/2013
Title:	TUSB8040A Data Sheet		
Customer Contact:	PCN Manager	Phone:	+1(214) 480-6037
		Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process
<input type="checkbox"/>	Design	<input checked="" type="checkbox"/>	Electrical Specification
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Part number change

PCN Details

Description of Change:

The product datasheet(s) is being update, to add feature and add VBUS max.

The following change history provides further details. These changes may be reviewed at the datasheet links provided

From (Page 5):

1 PRODUCT OVERVIEW

1.1 Features

- Supports On-Board and In-System EEPROM Programming Via the USB 2.0 Upstream Port
- Single Clock Input, 24-MHz Crystal or Oscillator

To (Page 5):

1 PRODUCT OVERVIEW

1.1 Features

- Supports On-Board and In-System EEPROM Programming Via the USB 2.0 Upstream Port
- Single Clock Input, 24-MHz Crystal or Oscillator
- **No Special Driver Requirements; Works Seamlessly With Any Operating System With USB Stack Support**

From (Page 9):

2.3 USB Upstream Signals

Table 2-3. USB Upstream Signals

USB_R1	PT	A50	Precision resistor reference. A 9.09-k Ω \pm 1% resistor should be connected between USB_R1 and USB_R1RTN.
USB_R1RTN	PT	B47	Precision resistor reference return
USB_VBUS	I	B44	USB upstream port power monitor. The VBUS detection requires a voltage divider. The signal USB_VBUS must be connected to VBUS through a 90.9-k Ω \pm 1% resistor, and to ground through a 10-k Ω \pm 1% resistor from the signal to ground.

To (Page 9):

2.3 USB Upstream Signals

Table 2-3. USB Upstream Signals

USB_R1	PT	A50	Precision resistor reference. A 9.09-k Ω \pm 1% resistor should be connected between USB_R1 and USB_R1RTN.
USB_R1RTN	PT	B47	Precision resistor reference return
USB_VBUS	I	B44	USB Upstream port power monitor. The USB_VBUS input is a 1.2-V I/O cell and requires a voltage divider to prevent damage to the input. The signal USB_VBUS must be connected to VBUS through a 90.9-k Ω \pm 1% resistor, and to signal ground through a 10-k Ω \pm 1% resistor. This allows the input to detect VBUS present from a minimum of 4 V and sustain a maximum VBUS voltage up to 10 V (applied to the voltage divider).

From (page 28):

6.1 Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

		VALUE	UNIT
V _{DD33}	Supply voltage	-0.3 to 3.8	V
V _{DD11}		-0.3 to 1.4	
T _{stg}	Storage temperature range	-65 to 150	°C

To (page 28):

6.1 Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

		VALUE	UNIT
V _{DD33}	Steady-state supply voltage	-0.3 to 3.8	V
V _{DD11}		-0.3 to 1.4	
V _{IO}	USB 2.0 DP/DM	-0.3 to V _{DD33} + 0.3 \leq 3.8	V
	SuperSpeed USB TXP/M and RXP/M	-0.3 to V _{DD33} + 0.3 \leq 3.8	
	XI/XO	-0.3 to 1.98	
	3.3-V Tolerant I/O	-0.3 to V _{DD33} + 0.3 \leq 3.8	
V _{USB_VBUS}		-0.3 to 1.2	V
T _{stg}	Storage temperature range	-65 to 150	°C

From (page 28):

6.2 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{DD33}	Supply voltage	3	3.3	3.6	V
V _{DD11} ⁽¹⁾		0.99	1.100	1.26	
T _A	Operating free-air temperature range	0	25	70	°C
T _J	Operating junction temperature range	0	25	105	°C

To (page 28):

6.2 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{DD33}	Steady-state supply voltage	3	3.3	3.6	V
V _{DD11} (1)		0.99	1.1	1.26	
V _{IO}	USB 2.0 DP/DM	0		VDD33	V
	SuperSpeed USB TXP/M and RXP/M	0		VDD33	
	XI/XO	0		1.8	
	3.3-V Tolerant I/O	0		VDD33	
V _{USB_VBUS}		0		1.155	V
T _A	Operating free-air temperature range	0	25	70	°C
T _J	Operating junction temperature range	0	25	105	°C

The datasheet number will be changing.

Device Family	Change From:	Change To:
TUSB8040A	SLLSEA7E	SLLSEA7F

The updated datasheet(s) can be accessed by the following link(s):

<http://www.ti.com/product/tusb8040a>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None

Product Affected:

TUSB8040ARKMR	TUSB8040ARKMT
---------------	---------------

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com