

Notification Number:	20210222002	Notification Date:	Mar. 24, 2021
Title:	Datasheet for TLV320AIC3104		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:	Electrical Specification		

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TLV320AIC3104

SLAS510G – MARCH 2007 – REVISED FEBRUARY 2021

Changes from Revision F (December 2016) to Revision G (February 2021)	Page
• Updated the numbering format for tables, figures, and cross-references throughout the document	1
• Changed QFN to VQFN throughout document.....	1
• Changed <i>Applications</i> section	1
• Changed <i>Device Comparison Table</i> : changed title, added TLV320AIC3109-Q1 row.....	4
• Deleted <i>System Thermal Characteristics</i> table.....	8
• Added <i>input impedance</i> parameter in <i>Electrical Characteristics</i> table: added <i>single-ended</i> to test conditions of first two rows, added last two rows to parameter.....	8
• Deleted current consumption parameter, <i>Stereo line in to stereo line out , no signal</i> test condition.....	8
• Changed list of intended applications in <i>Overview</i> section.....	19
• Added <i>Functional Block Diagram With Registers</i> figure and added caption to <i>Functional Block Diagram</i> figure	20
• Added note to <i>Audio Clock Generation</i> section.....	26
• Changed 2 MHz to 512 kHz in $512 \text{ kHz} \leq (\text{PLLCLK}_{\text{IN/P}}) \leq 20 \text{ MHz}$ PLL example in <i>Audio Clock Generation</i> section.....	26
• Added <i>Left Channel Signal Path</i> and <i>Right Channel Signal Path</i> figures to <i>Audio Analog Inputs</i> section.....	35
• Deleted <i>Analog Input Bypass Path Functionality</i> section.....	43
• Changed <i>Passive Analog Bypass Mode Configuration</i> figure to remove LINE 2L/R input bypass	43
• Added reset value to D0 row in <i>Page 0, Register 9: Audio Serial Data Interface Control Register B</i> table.....	48
• Changed D3–D0 row reset value from 000 to 0000 in <i>Page 0, Register 37: DAC Power and Output Driver Control Register</i> table.....	48
• Changed reset value from 1 to 0 and changed description of bit D1 in <i>Page 0, Register 51: HPLOUT Output Level Control Register</i>	64
• Changed reset value from 1 to 0 and changed description of bit D1 in <i>Page 0, Register 58: HPLCOM Output Level Control Register</i>	64
• Changed reset value from 1 to 0 and changed description of bit D1 in <i>Page 0, Register 65: HPROUT Output Level Control Register</i>	64
• Changed reset value from 1 to 0 and changed description of bit D1 in <i>Page 0, Register 72: HPRCOM Output Level Control Register</i>	64

- Changed reset value from 1 to 0 and changed description of bit D1 in *Page 0, Register 86: LEFT_LOP/M Output Level Control Register* 64
- Changed Read/Write value from R to R/W in bit D0 of *Page 0, Register 86: LEFT_LOP/M Output Level Control Register* 64
- Changed reset value from 1 to 0 and changed description of bit D1 in *Page 0, Register 93: RIGHT_LOP/M Output Level Control Register* 64
- Changed Read/Write value from R to R/W in bit D0 of *Page 0, Register 93: RIGHT_LOP/M Output Level Control Register* 64
- Changed Read/Write value from R to R/W in bit D0 of *Page 0, Register 96: Sticky Interrupt Flags Register* 64
- Changed reset value from 00 to 11 and changed description of bits D5–D4 in *Page 0, Register 107: New Programmable ADC Digital Path and I²C Bus Condition Register* 64
- Changed description of bits D6 and D2 in *Page 0, Register 108: Passive Analog Signal Bypass Selection During Power Down Register* 64
- Changed *Cell Phone* to *Portable* in title of *Typical Connections With Headphone and External Speaker Driver in Portable Application* section and in title of *Typical Connections With Headphone and External Speaker Driver in Portable Applications* figure..... 88

The datasheet number will be changing.

Device Family	Change From:	Change To:
TLV320AIC3104	SLAS510F	SLAS510G

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TLV320AIC3104>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

HPA01211RHBR	TLV320AIC3104IRHBR	TLV320AIC3104IRHBT	
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