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|-----------------------------|-----------------------------|-------------------------------------|---------------------|
| Notification Number: | 20161121000 | Notification Date: | November 29, 2016 |
| Title: | Datasheet for TLV620612-Q1 | | |
| Customer Contact: | PCN Manager | Dept: | Quality Services |
| Change Type: | | | |
| <input type="checkbox"/> | Assembly Site | <input type="checkbox"/> | Design |
| <input type="checkbox"/> | Assembly Process | <input checked="" type="checkbox"/> | Data Sheet |
| <input type="checkbox"/> | Assembly Materials | <input type="checkbox"/> | Part number change |
| <input type="checkbox"/> | Mechanical Specification | <input type="checkbox"/> | Test Site |
| <input type="checkbox"/> | Packing/Shipping/Labeling | <input type="checkbox"/> | Test Process |
| | | <input type="checkbox"/> | Wafer Bump Site |
| | | <input type="checkbox"/> | Wafer Bump Material |
| | | <input type="checkbox"/> | Wafer Bump Process |
| | | <input type="checkbox"/> | Wafer Fab Site |
| | | <input type="checkbox"/> | Wafer Fab Materials |
| | | <input type="checkbox"/> | Wafer Fab Process |

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is updated as seen in the change revision history below:



TLV620612-Q1

SLVSB93C –JANUARY 2012–REVISED NOVEMBER 2016

www.ti.com

| Changes from Revision B (December 2014) to Revision C | Page |
|--|------|
| • Changed the maximum efficiency from 97% to 88% | 1 |
| • Changed the maximum output current from 2 A to 1.6 A throughout the data sheet | 1 |
| • Changed the FB pin description in the <i>Pin Functions</i> table | 4 |
| • Changed the output voltage in the <i>Recommended Operating Conditions</i> to 1.2 V and removed all references to other output voltages (V_{OUT}). Updated graphics where applicable..... | 5 |
| • Deleted the junction temperature from the <i>Recommended Operating Conditions</i> table | 5 |
| • Changed the <i>Thermal Information</i> values | 5 |
| • Changed the typical reference voltage value from 1.2 V to 600 mV | 6 |
| • Changed graphs to show input voltage up to 5.5 V instead of 6 V in the <i>Typical Characteristics</i> section | 8 |
| • Deleted the <i>Parameter Measurement Information</i> section because the circuit is similar to the application circuit in the <i>Typical Application</i> section | 11 |
| • Added a description of the clock dithering feature to the <i>Feature Description</i> section..... | 12 |
| • Deleted the <i>100% Duty Cycle Low-Dropout Operation</i> section from the <i>Feature Description</i> section | 12 |
| • Deleted the <i>Output Voltage Setting</i> section in the <i>Detailed Design Procedure</i> section..... | 15 |
| • Added the <i>Receiving Notification of Documentation Updates</i> section | 19 |
| • Changed the ESDS caution statement | 19 |

The datasheet number will be changing.

| | | |
|---------------|--------------|-----------------|
| Device Family | Change From: | Change To: |
| TLV620612-Q1 | SLVSB93B | SLVSB93C |

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/lit/ds/symlink/tlv620612-q1.pdf>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device

Changes to product identification resulting from this notification:

None.

Product Affected:

TLV620612TDSGRQ1

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

| Location | E-Mail |
|-----------------|--|
| USA | PCNAmericasContact@list.ti.com |
| Europe | PCNEuropeContact@list.ti.com |
| Asia Pacific | PCNAsiaContact@list.ti.com |
| Japan | PCNJapanContact@list.ti.com |